I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING FACSIMILE TRANSMITTED TO THE USPTO ON

| TO THE UNITED STATES PATENT AND TRADEMARK OFFICES. | |
|--|---|
| in Re Appacation of Bolam et al. | May 17, 2002 |
| Senal No. 09/878,681 Filing Date: June 11, 2001 | Group Att Unit 2824 |
| SILICON-ON-INSULATOR CHIP HAVING AN ISOLATION BARRIER FOR RELIABILITY | IBM Corporation 2070 Route 52 Dept. 18G, Bldg. 300-482 Hopewell Junction New York 12533 |

RESPONSE

FAX COPY RECEIVED MAY 17 2002

The Commissioner for Patents Washington, D.C. 20231

TECHNOLOGY CENTER 2800

Sir:

In response to the Office Action dated 3/05/02, applicant replies as follows:

REMARKS

Applicant submits a Terminal Disclaimer referencing commonly-owned U.S. Patent No.